

FIG. 1

FIG. 2

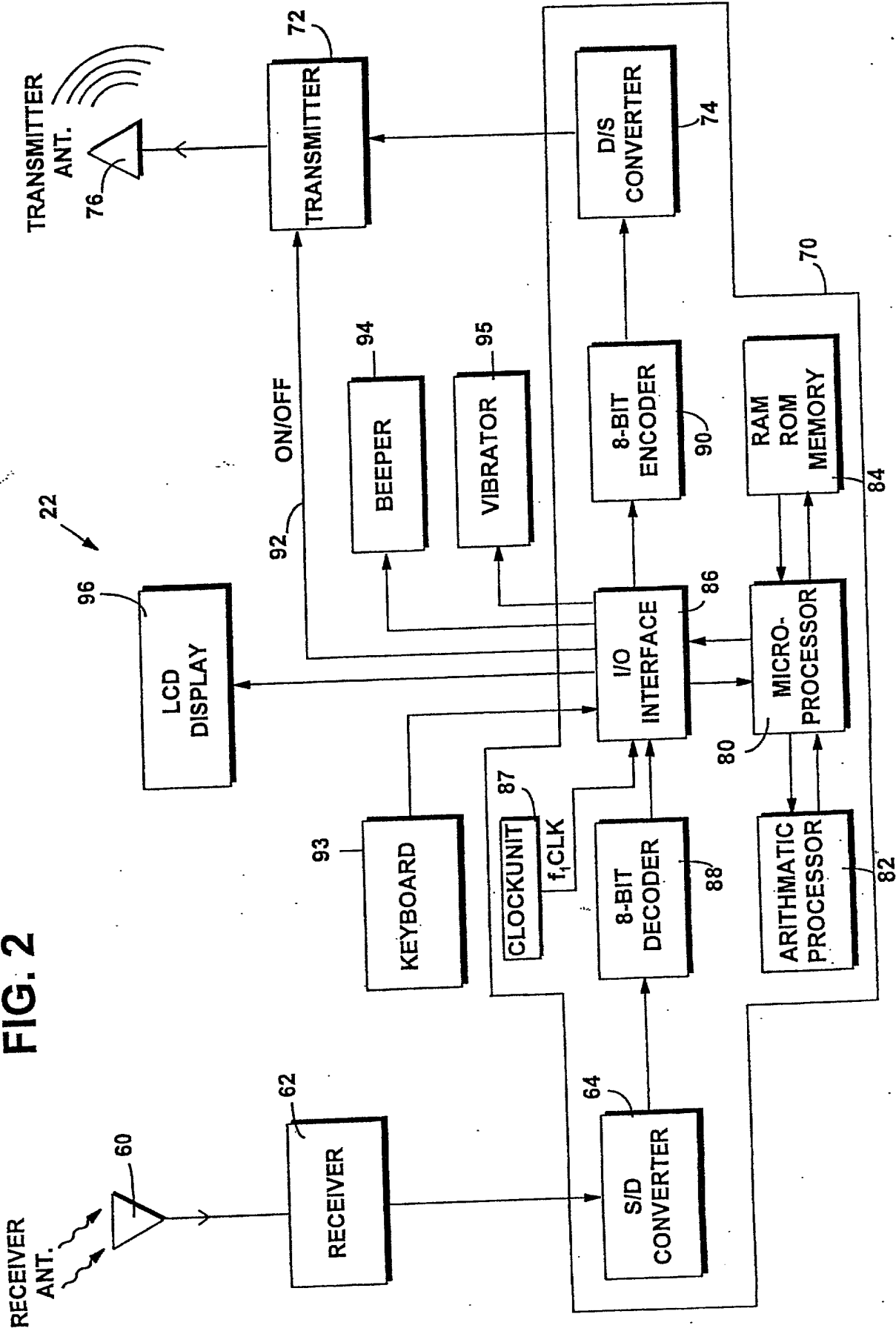
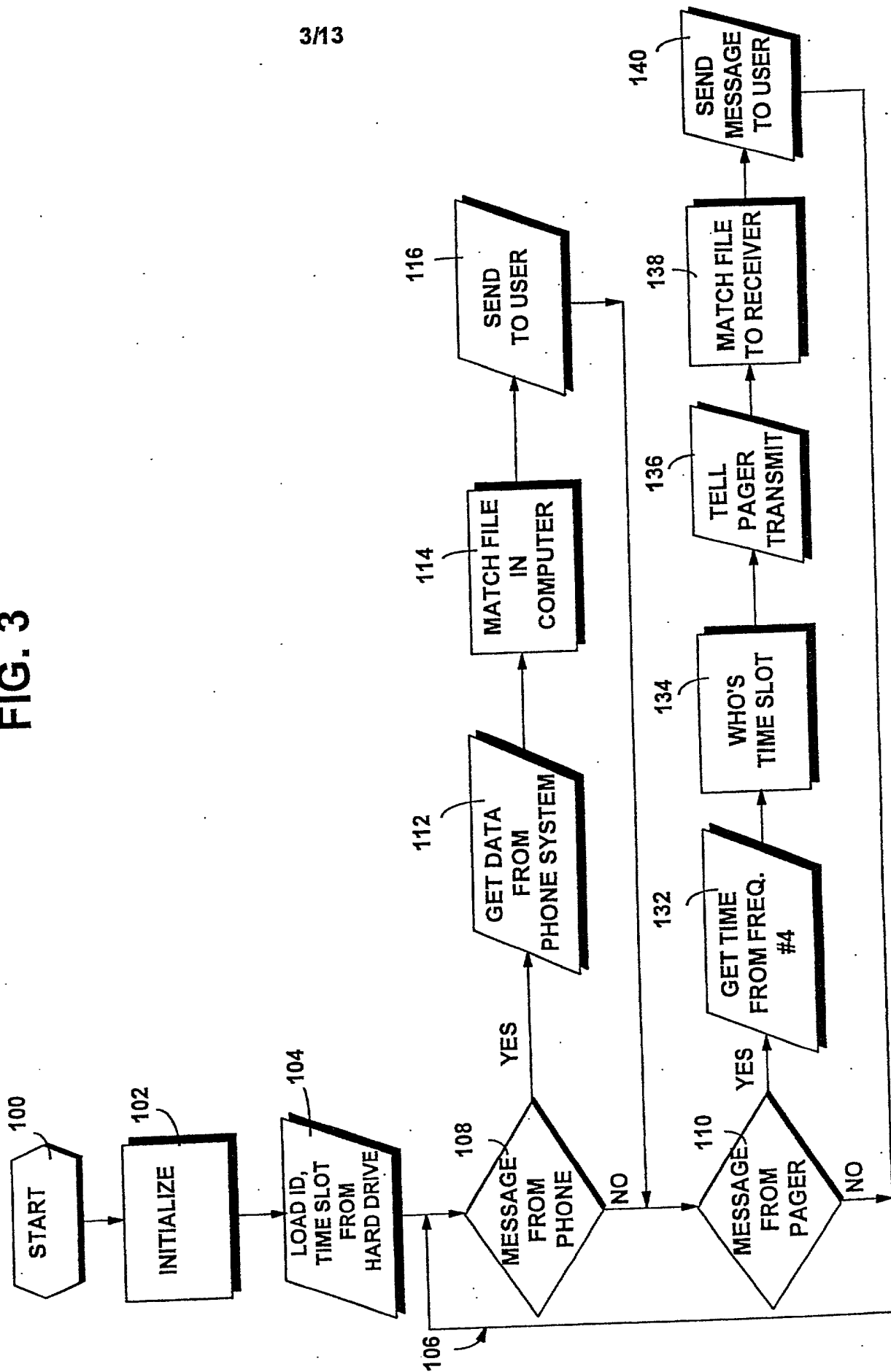


FIG. 3



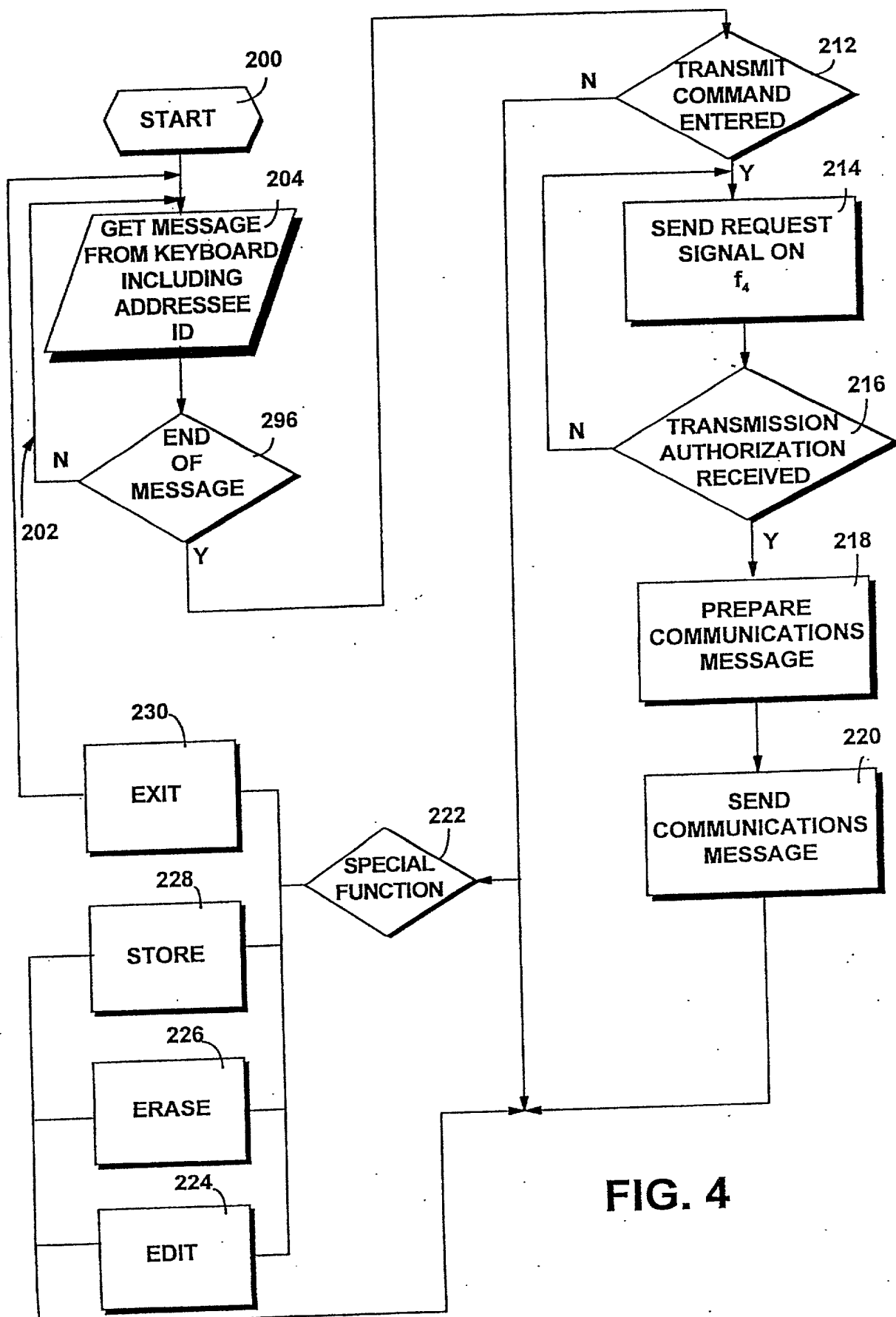
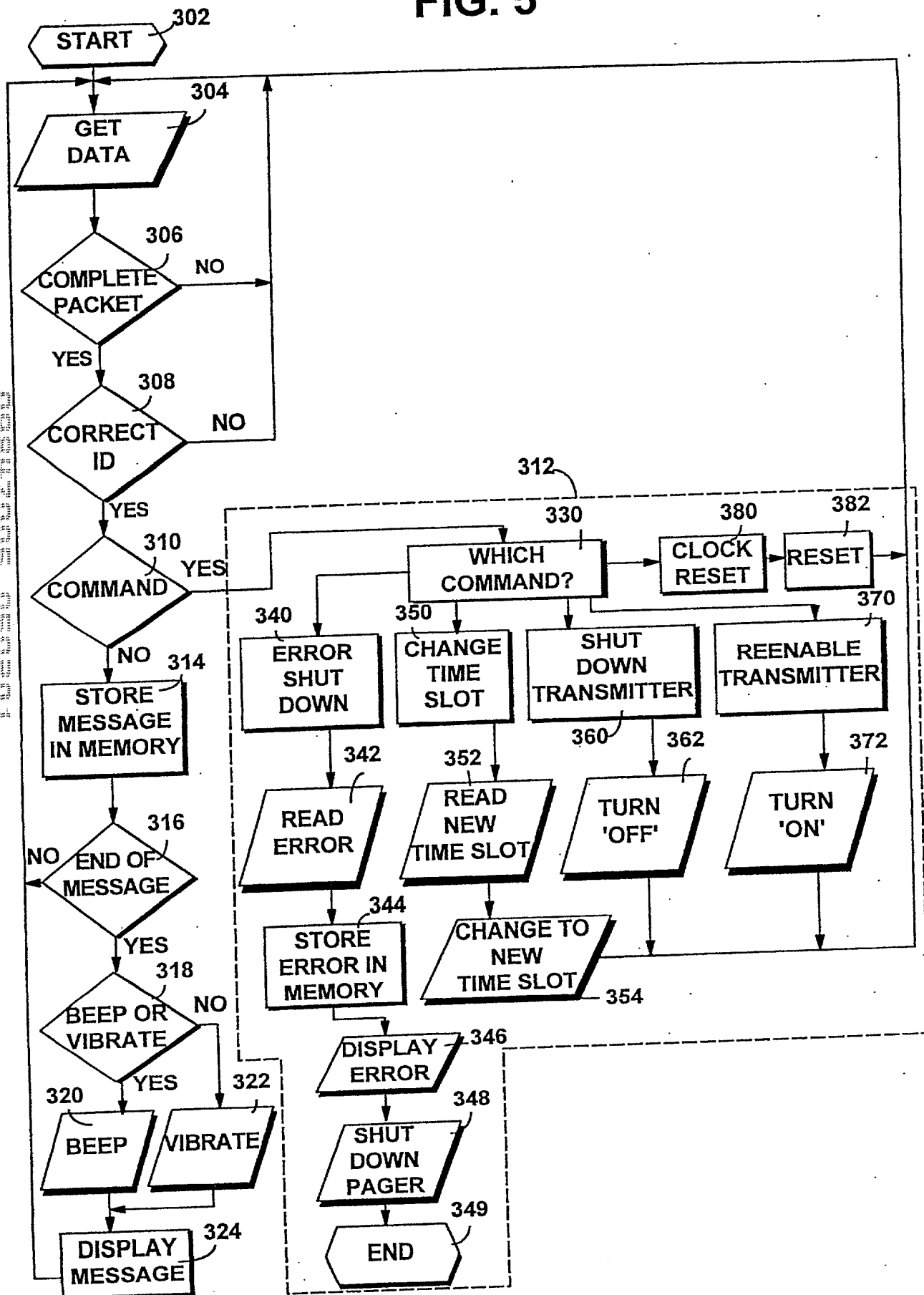


FIG. 4

FIG. 5



# FIG. 6

6/13

CLOCK TRANSMITTER FREQUENCY  $f_1$

CENTRAL COMPUTER TRANSMITTER  
FREQUENCY  $f_2$

PAGER TRANSMITTER FREQUENCY  $f_3$

PAGER REQUEST FLAG TRANSMITTER  
FREQUENCY  $f_4$

TIME

$P_1$  TELL THE COMPUTER IT GOT THE  
REPLY (STEP 140)

$P_1$  PERMISSION SIGNAL (STEP 136)

$P_1$  REQUESTING TO SEND (STEP 214)

COMPUTER TELLS  $P_1$  HAD RECEIVED THE  
MESSAGE (STEP 140)

$P_2$  TELLS THE COMPUTER THAT IT GOT THE  
MESSAGE (STEP 220)

GIVE  $P_2$  PERMISSION TO SEND (STEP 136)

$P_2$  REQUEST TO TRANSMIT (STEP 214)

COMPUTER SENDING MESSAGE TO  $P_2$  (STEP 140)

$P_1$  SENDING MESSAGE TO COMPUTER (STEP 220)

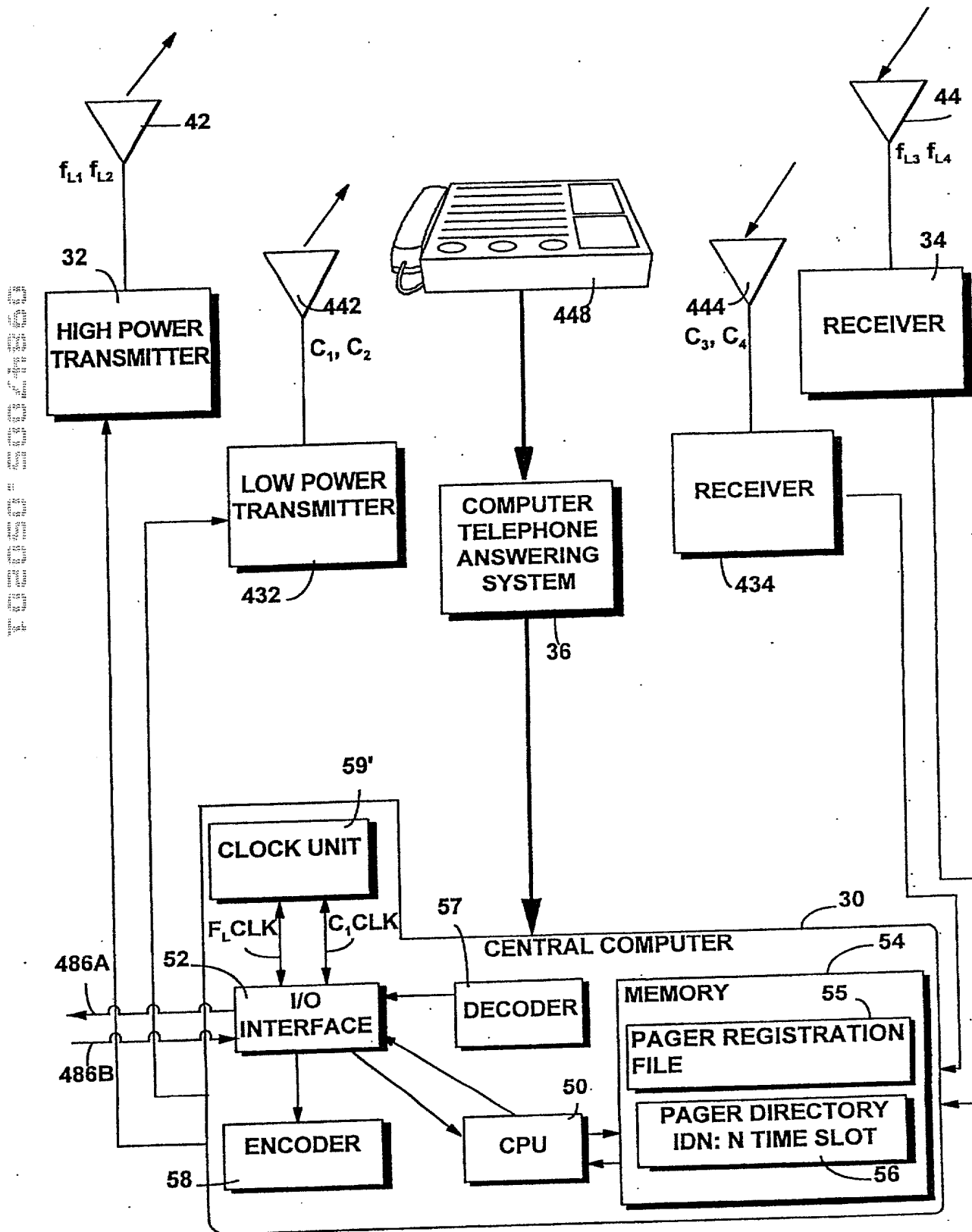
GIVE  $P_1$  PERMISSION TO TRANSMIT (STEP 136)

$P_1$  REQUEST TO TRANSMIT (STEP 214)

FIG. 6

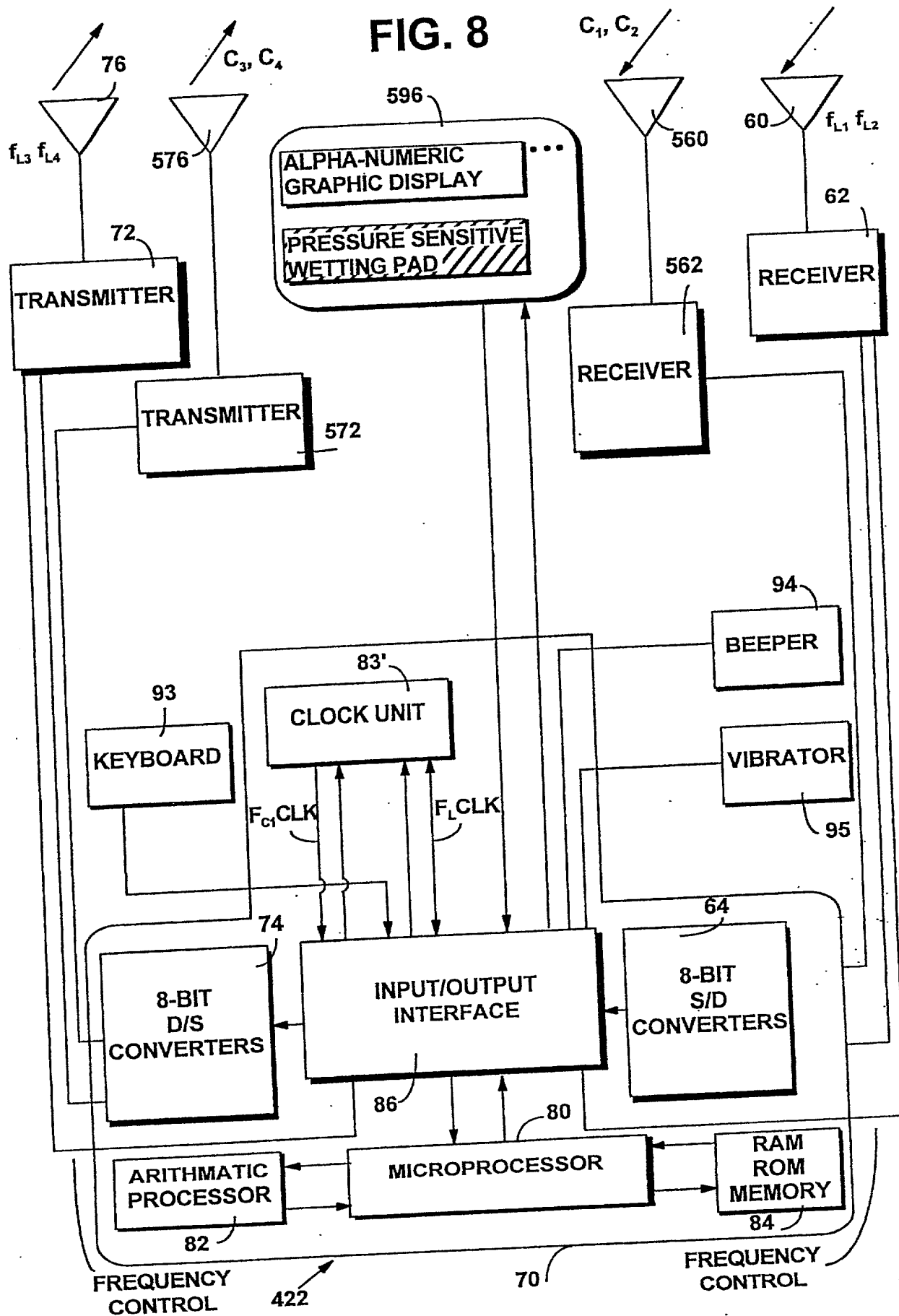
FIG. 7

7/13.



# FIG. 8

8/13





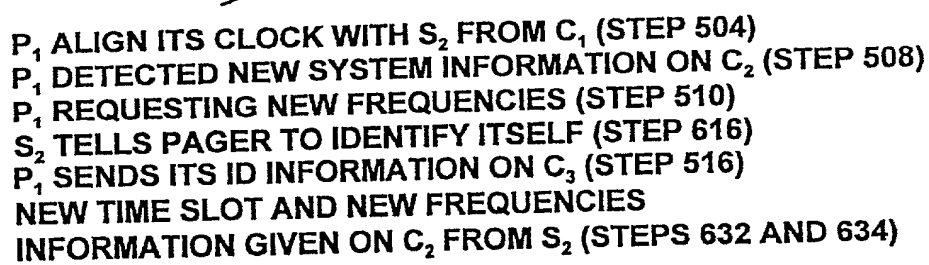
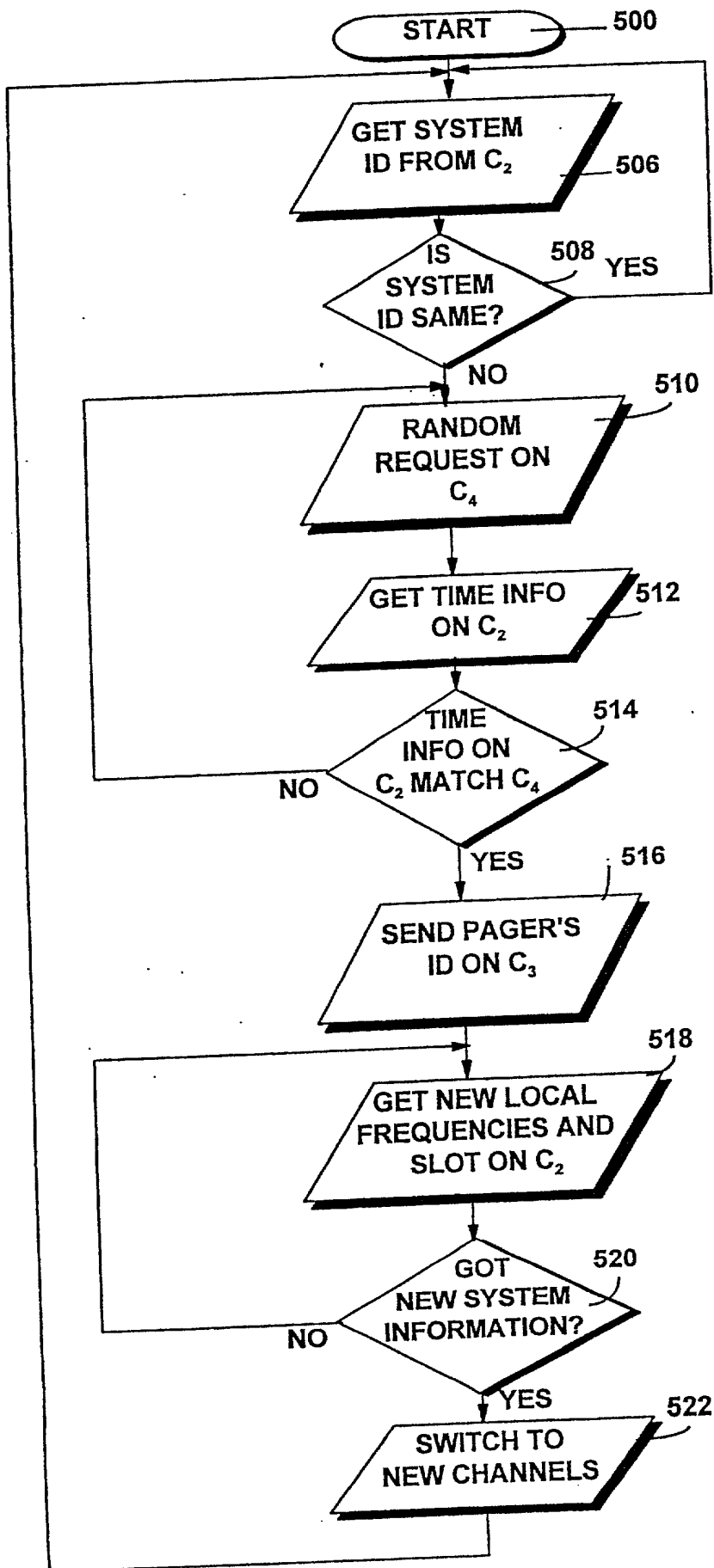
[illegible]

FIG. 10



# FIG. 11

11/13

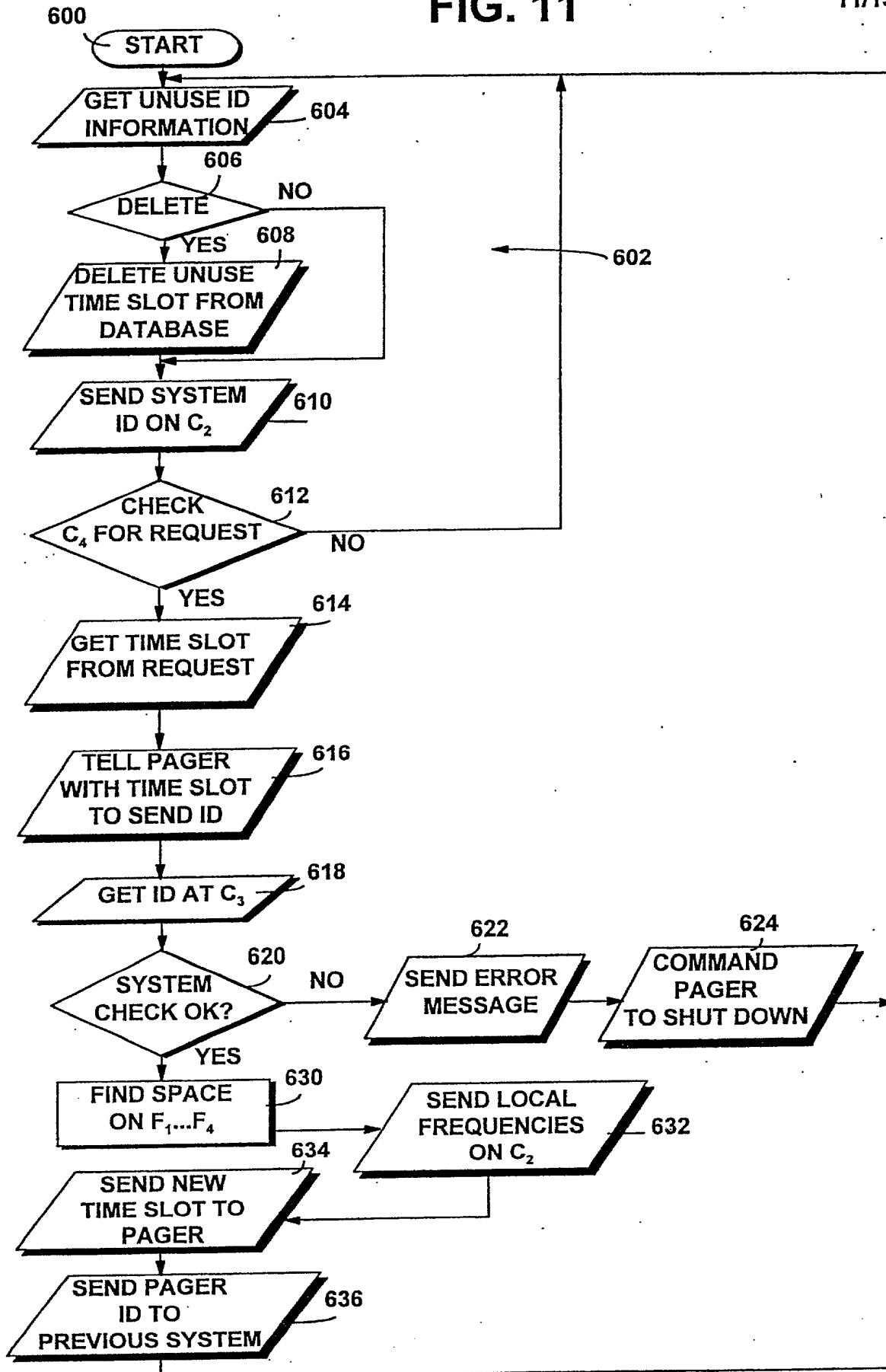


FIG. 12



FIG. 13

